

109822-97

10/749,571

LIST OF PATENTS AND OTHER ITEMS FOR APPLICANT'S  
INFORMATION DISCLOSURE STATEMENT

APPLICANT:

Mikko Waltari

FILING DATE:

Filed herewith

GROUP:

To be assigned

(Use several sheets if necessary)

## U.S. PATENT DOCUMENTS

Examiner Initials	Cite No.	Document Number	Publication Date/ Issue Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number-Kind Code			
JBW	AA	5,212,486	05-18-1993	Nagaraj	
	AB	5,510,789	04-23-1996	Lee	
	AC	5,644,308	07-01-1997	Kerth et al.	
	AD	5,861,832	01-19-1999	Nagaraj	
	AE	US 6,535,157B1	03-18-2003	Garrity et al.	

## FOREIGN PATENT DOCUMENTS

Examiner Initials	Cite No.	Foreign Patent Document	Publication Date/Issue Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T <sup>1</sup>

## NON PATENT LITERATURE DOCUMENTS

Examiner Initials	Cite No.	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T <sup>1</sup>
JBW	AF	KRISHNASWAMI NAGARAJ, Senior Member; Efficient Circuit Configurations for Algorithmic Analog to Digital Converters; IEEE Transactions on Circuits and Systems-II: Analog and Digital Signal Processing; December 1993; pp. 1-9; Vol. 40, No. 12 <i>No Date</i>	

EXAMINER:

*Patrick C. Wamey*

DATE CONSIDERED:

*02/01/2006*

EXAMINER: Please initial if reference is considered, whether or not citation is in conformance with MPEP 609; draw line through citation if not in conformance and not considered. Include a copy of this form with next communication to applicant. Thank you.

<sup>1</sup>Applicant is to place a check mark here if English language translation is attached.